SEP 2.7 2006 ESTATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
FUKUOKA et al.)
Application Number: 10/619,463) Art Unit 2186
Filed: July 16, 2003)
For: SEMICONDUCTOR INTEGRATED CIRCUIT	Examiner Iwashko, Lev
Attorney Docket No. HITA.0408)

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

COVER LETTER

Sir:

[x] The fee for submission of claims is calculated as shown below:

For	TOTAL WITH NEW CLAIMS ADDED	TOTAL CURRENTLY ON FILE	CLAIMS ALREADY PAID	RATE	CALCULATION
Total Claims	9	9	(Over 20)	x \$50	0
Independent Claims	2	2	(Over 3)	x \$200	0
MULTIPLE DEPENDENT CLAIM(S)				+ \$360	0
REDUCTION FOR FILING BY SMALL ENTITY (note 37 C.F.R. §§ 1.9, 1.27, 1.28).		x ½			
TOTAL		0			

In addition, the below-identified communications are submitted in the above-captioned application or proceeding:

[x] Response to Office Action	[] Petition for Extension of Time (month)
(with Claim Election)	[] Terminal Disclaimer
Substitute Specification	[] Letter to Draftsperson
Preliminary Amendment	[] Assignment
[] Information Disclosure Statement	[] Other

[]	Please charge my Deposit Account Number A duplicate copy of	in the amount of to cover the fees for this paper is enclosed.
[]	A check in the amount of \$ to cover to	he fee is enclosed.
[x]		d to charge any additional fees associated with this F.R. § 1.16 and 1.17, or credit any overpayment to Deposit
PE 27	R R	espectfully submitted,
Evrene		tanley P. Fisher egistration Number 24,344

REED SMITH LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 September 27, 2006

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RESPONSE TO RESTRICTION REQUIREMENT

Sir:

This is in response to the Office Action mailed on August 30, 2006, the period of response to which is set to expire on September 30, 2006. Applicants hereby elect the continued prosecution of the invention identified as claims 15-23, without traverse.